



**FIG. 1**

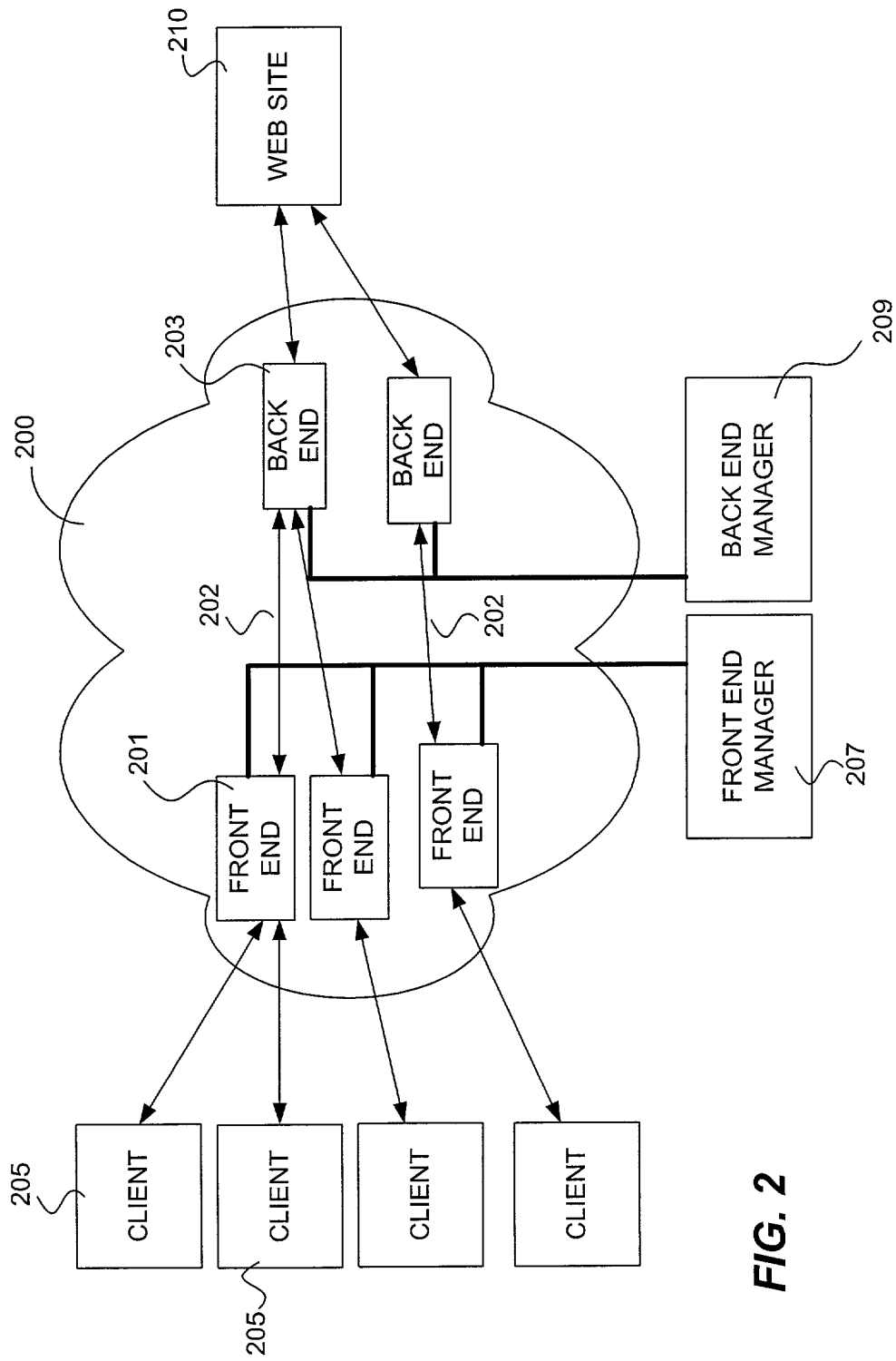


FIG. 2

FIG. 2 is a block diagram of a system architecture.

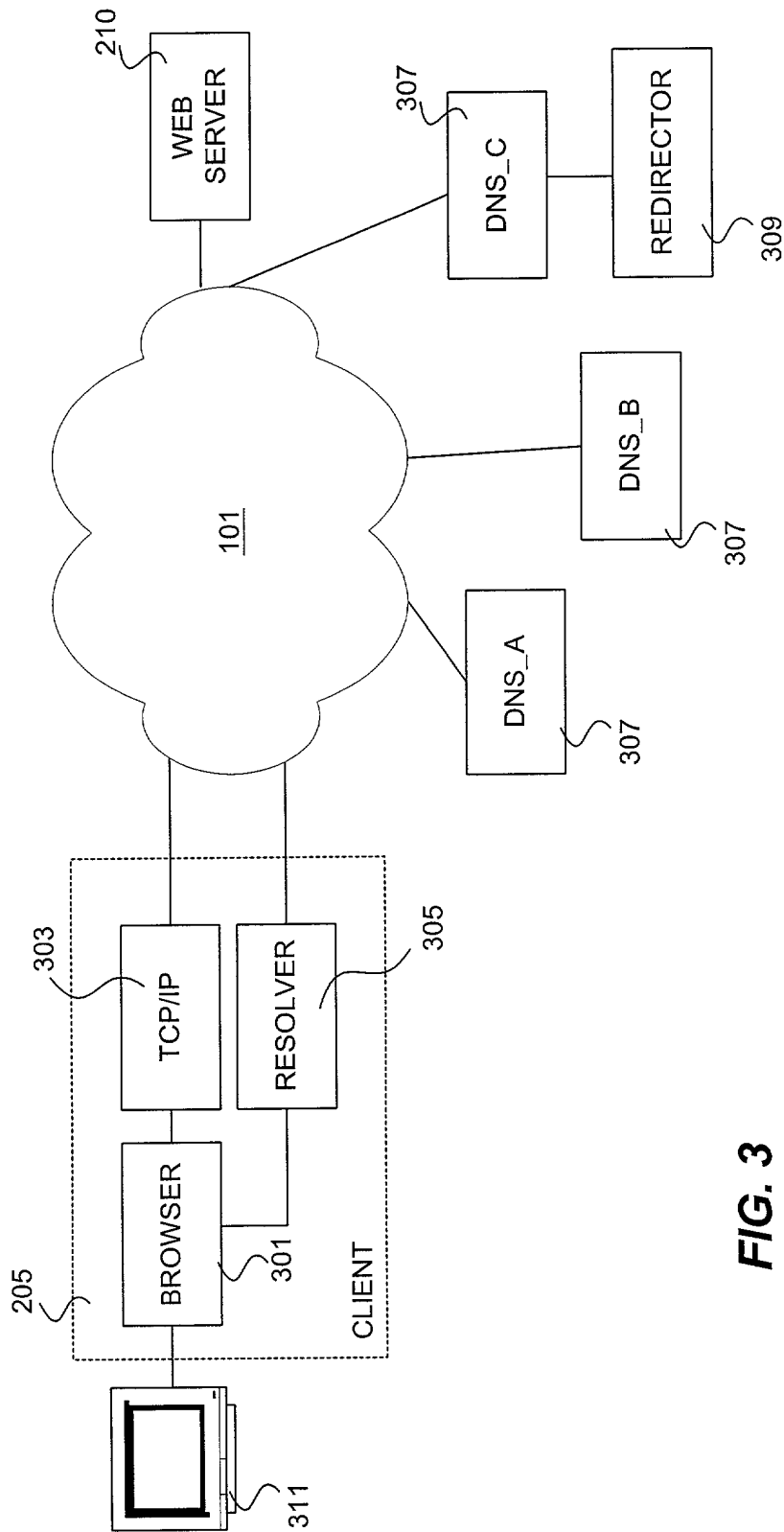


FIG. 3

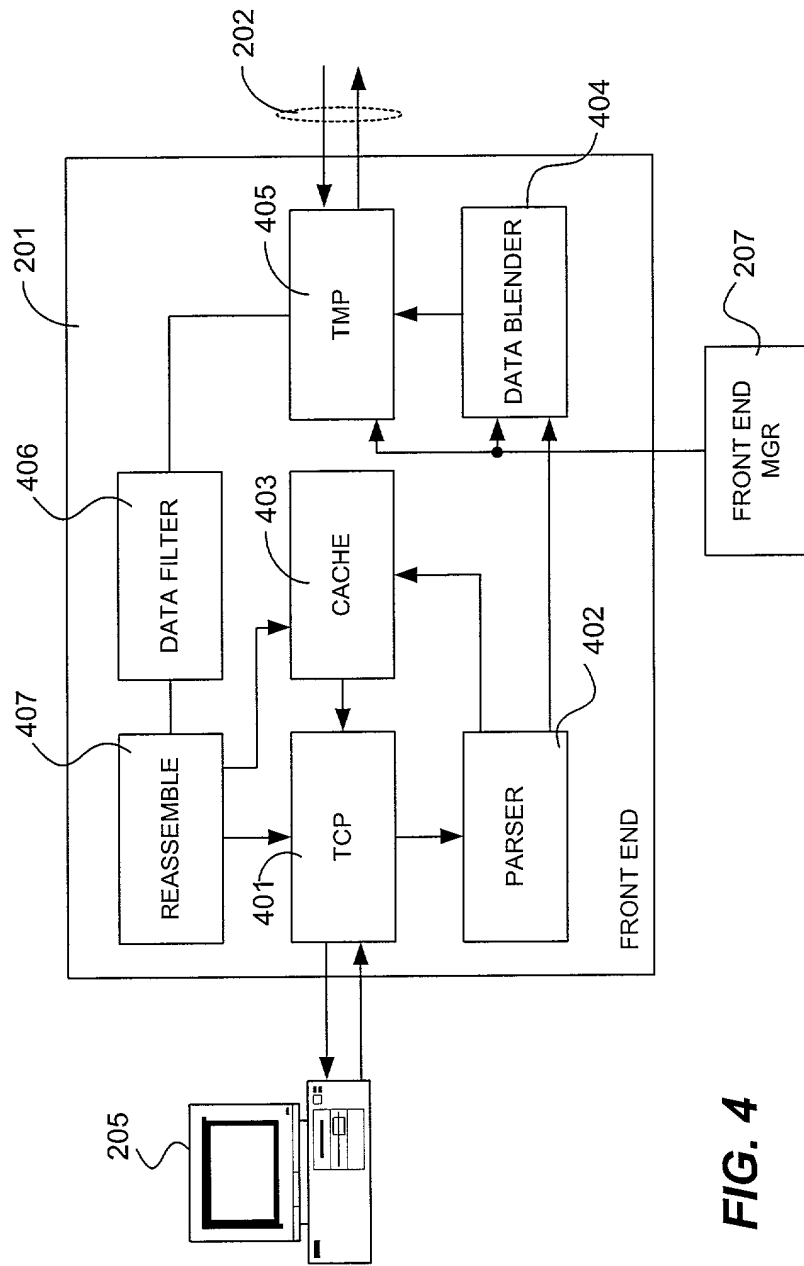


FIG. 4

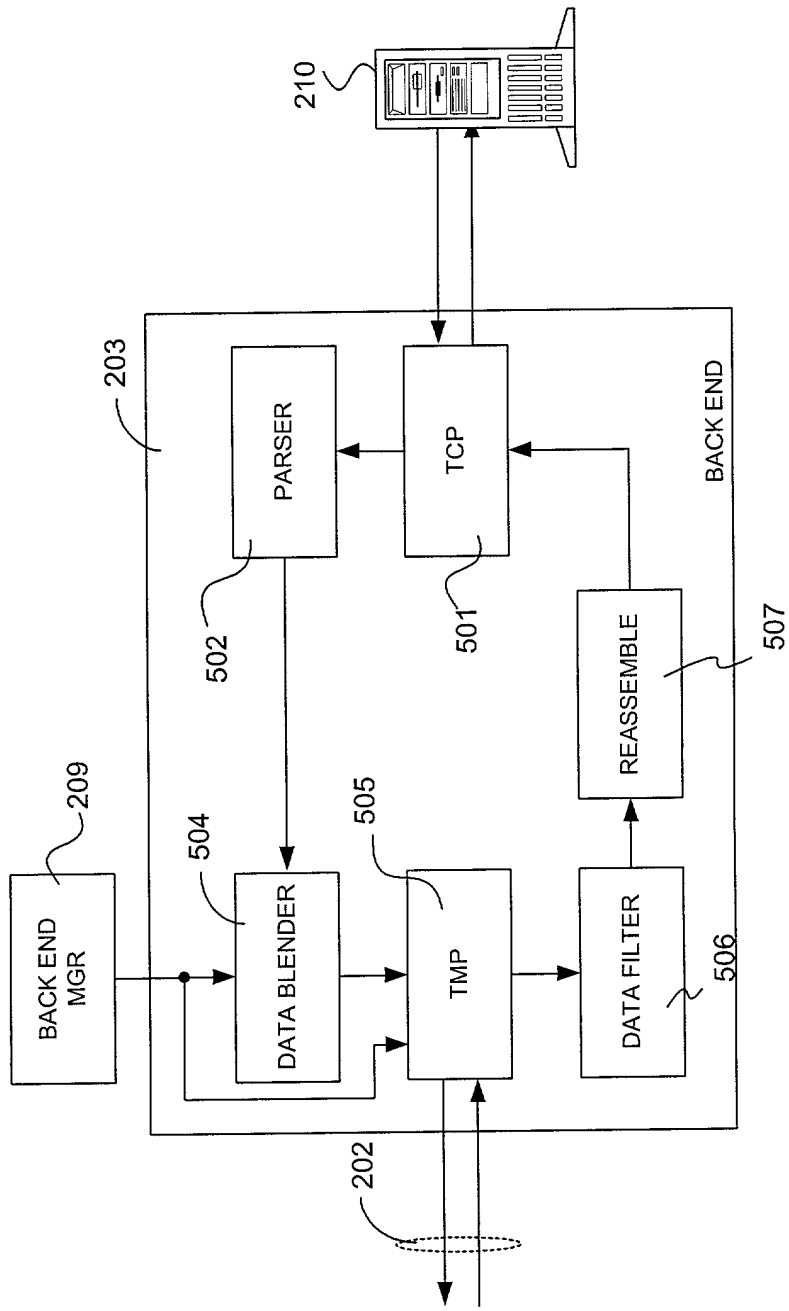


FIG. 5

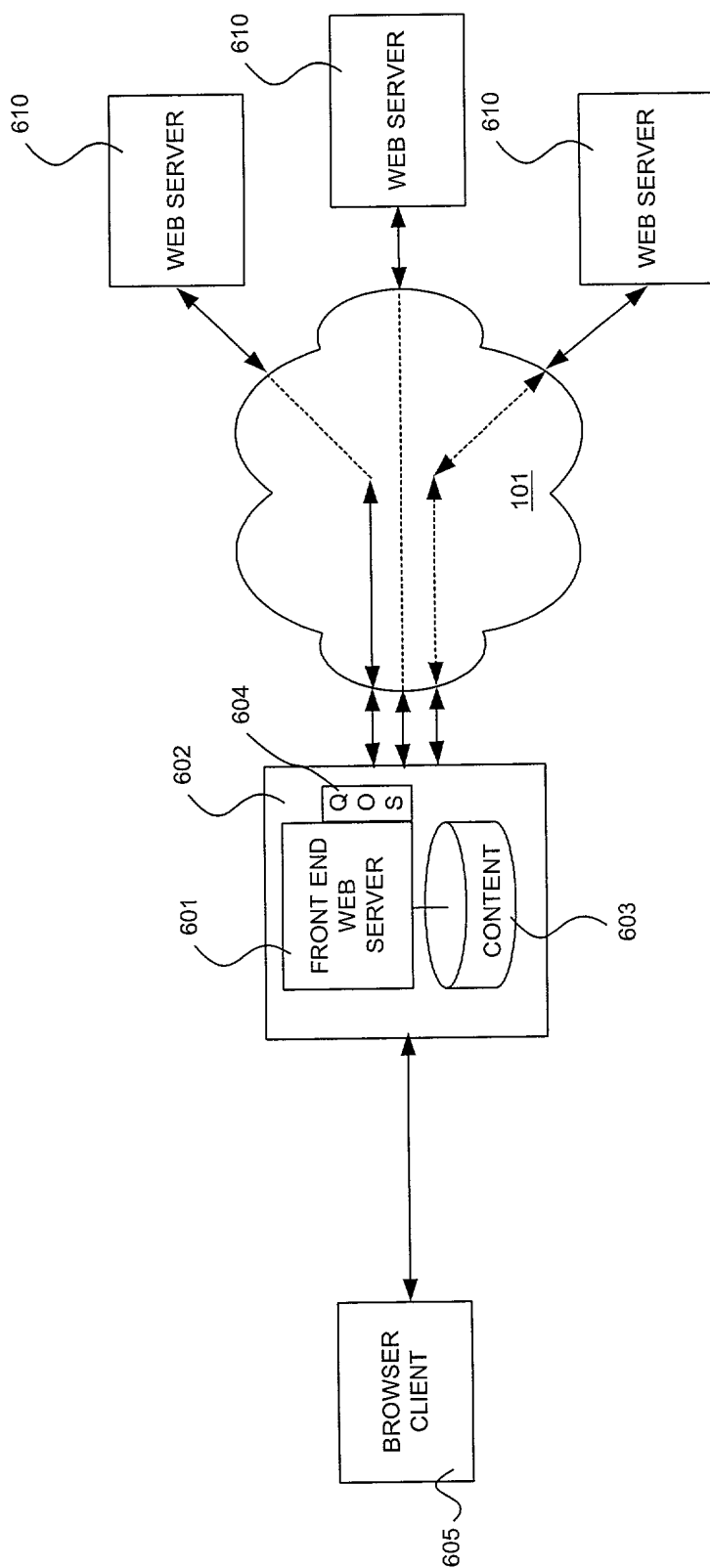


FIG. 6

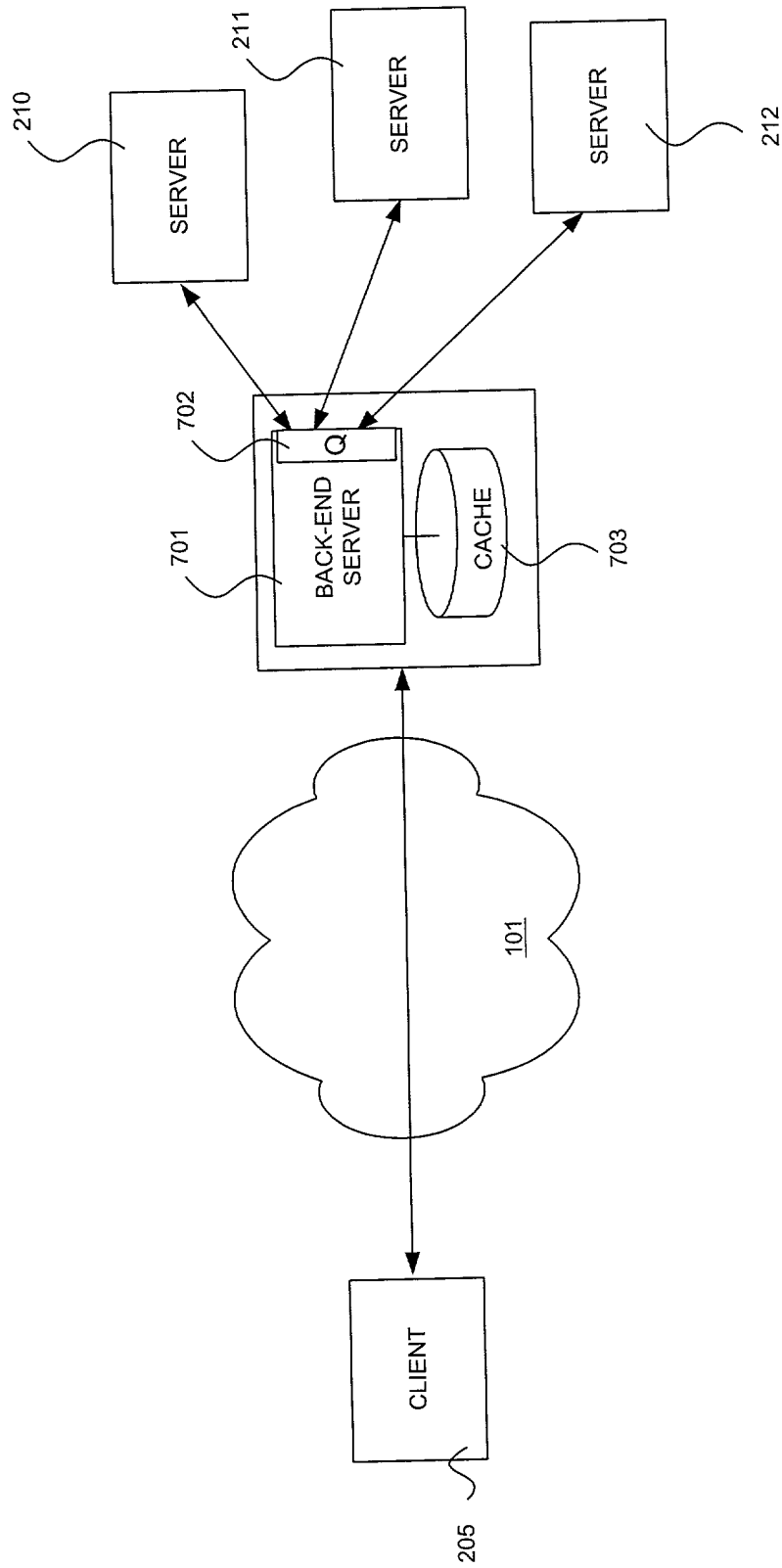


FIG. 7